

| Ref # | Hits | Search Query | DBs | Default Operator | Plurals | Time Stamp |
|-------|-------|--|---|------------------|---------|------------------|
| L1 | 23813 | "711"/\$.ccls. | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/02/16 19:22 |
| L2 | 6416 | "711"/\$.ccls. and module | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/02/16 19:22 |
| L3 | 1867 | ("711"/\$.ccls. and module) and row | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/02/16 19:23 |
| L4 | 979 | 711/5.ccls. | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/02/16 19:23 |
| L5 | 158 | (711/5.ccls. and module) and (arrang\$4 layout) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/02/16 19:23 |
| L6 | 58 | ((("711"/\$.ccls. and module) and row) and ((ic chip) near3 arrang\$4) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/02/16 19:23 |
| L7 | 1 | (711/5.ccls. and module) and ((first adj2 (buffer register)) with (first adj2 row)) and ((second adj2 (buffer register)) with (second adj2 row)) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/02/16 19:23 |
| L8 | 445 | ((second adj3 (buffer register)) near4 (data adj3 bit)) not (((second adj3 (buffer register)) near4 (data adj3 bit)) and memory and module) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/02/16 19:23 |